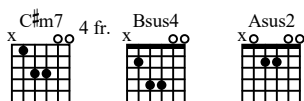


UNWINDING CABLE CAR

As recorded by Anberlin



A Intro

♩ = 130

Gtr I *mp*
let ring

T 0 4 6 4 0 4 6 4 0 4 6 4
A 6 6 6 6 6 6 6 6
B

let ring

T 0 4 6 4 0 4 6 4 0 4 6 4 0 4 6 4
A 6 6 6 6 6 6 6 6
B

Gtr III *mf*
let ring

T 9 8 6 9 (9)
A 6 9 8 6 9 (9)
B 4 6 9 8 6 9 (9) 7 9 11 5 7 9

let ring

T 0 4 6 4 0 4 6 4 0 4 6 4 7 0 (0)
A 6 6 6 6 6 6 6 6
B

let ring

T 9 8 6 9 (9) 9 8 6 9 (9) 9 8 6 9 (9)
A 6 9 8 6 9 (9) 6 9 8 6 9 (9)
B (9) 9 8 6 9 (9) 9 8 6 9 (9)

B Interlude

$\text{♩} = 131$

Gtr II *mf* C#m7 Bsus4 Asus2

Gtr IV *ff* *let ring*-----

11

T A B

9 8 6 9 (9) 7 9 11 (11) 7 9 11 9 (9)

sl. sl.

C Verse

C#m7 Bsus4 Asus2

15

T A B

7 9 9 (9) 11 (11) 7 9 9 11 9 (9)

sl. sl. sl. sl.

3x

C#m7 Bsus4 Asus2

19

T A B

7 9 9 (9) 11 (11) 7 7 9 7 (7) 9 7 6

sl. sl.

D Chorus

C#m7 Bsus4 Asus2

(2nd time) D.S.

23

let ring-----

T A B

9 8 6 9 (9) 7 9 11 (11) 7 9 11 9 (9)

sl. sl.

4x

E Bridge

Asus2 *mp* *let ring* Bsus4

27

ff

T 0 0 0 0 (0) 0 0 0 0 (0) 2 0 2 (2) 0 2 0 2

A 2 0 2 0 2 0 2 0 2 0 2 0 2

B

1. , 2.

Asus2 Bsus4

31

let ring

T 0 0 0 0 (0) 0 0 0 0 (0) 4 5 7 4 5 7 (7) 4 5 7 4 5 4 5

A 2 0 2 0 2 0 2 0 2 4 5 7 4 5 7 4 5 4 5

B

3.

Asus2 Bsus4

35

let ring

T 4 5 7 4 5 7 (7) 4 5 7 4 5 7 (7) 4 5 7 4 5 7 (7) 4 5 7 4 5 4 5

A

B

F Interlude

C#m7 Bsus4 Asus2

Gtr I *mp*

39

Gtr III *mf* *let ring*

T 9 8 6 9 (9) 7 9 11 (11) 7 9 11 9 (9)

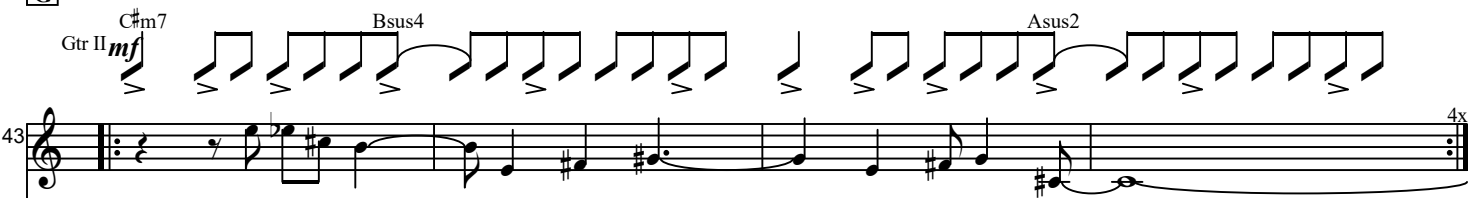
A

B

sl. *sl.*

G Chorus

Gtr II *mf* $C\sharp m7$ $Bsus4$ $Asus2$

43 

Gtr IV *f* *let ring*

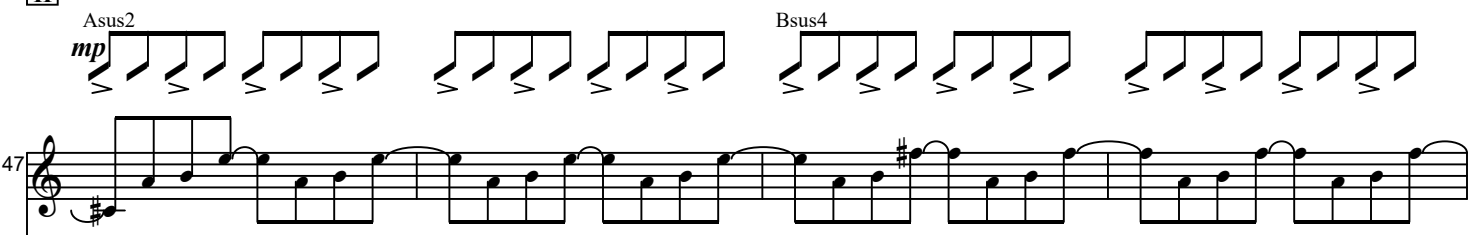
T 9 8 6 9 (9) 7 9 11 (11) 7 9 11 9 (9)

A

B *sl.* *sl.*

H Outro

$Asus2$ *mp* $Bsus4$

47 

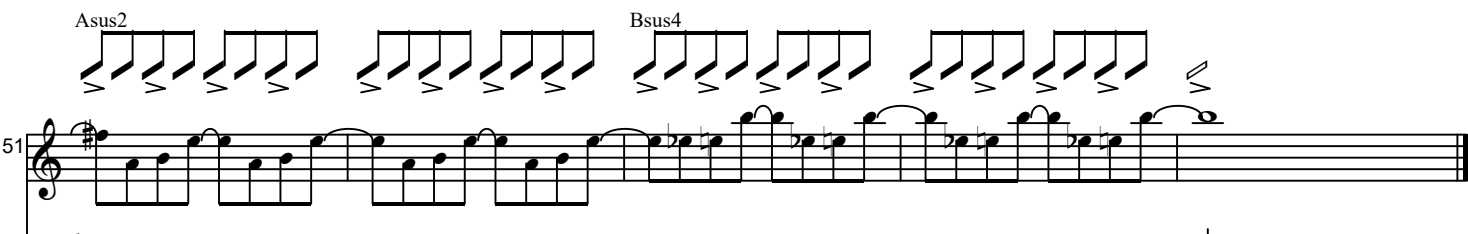
f *let ring*

T 2 0 0 2 0 0 (0) 2 0 0 2 0 0 (0) 2 0 2 2 0 2 (2) 2 0 2 2 0 2

A

B

$Asus2$ $Bsus4$

51 

let ring

T 2 0 0 2 0 0 (0) 2 0 0 2 0 0 (0) 4 5 7 4 5 7 (7) 4 5 7 4 5 7 (7)

A

B